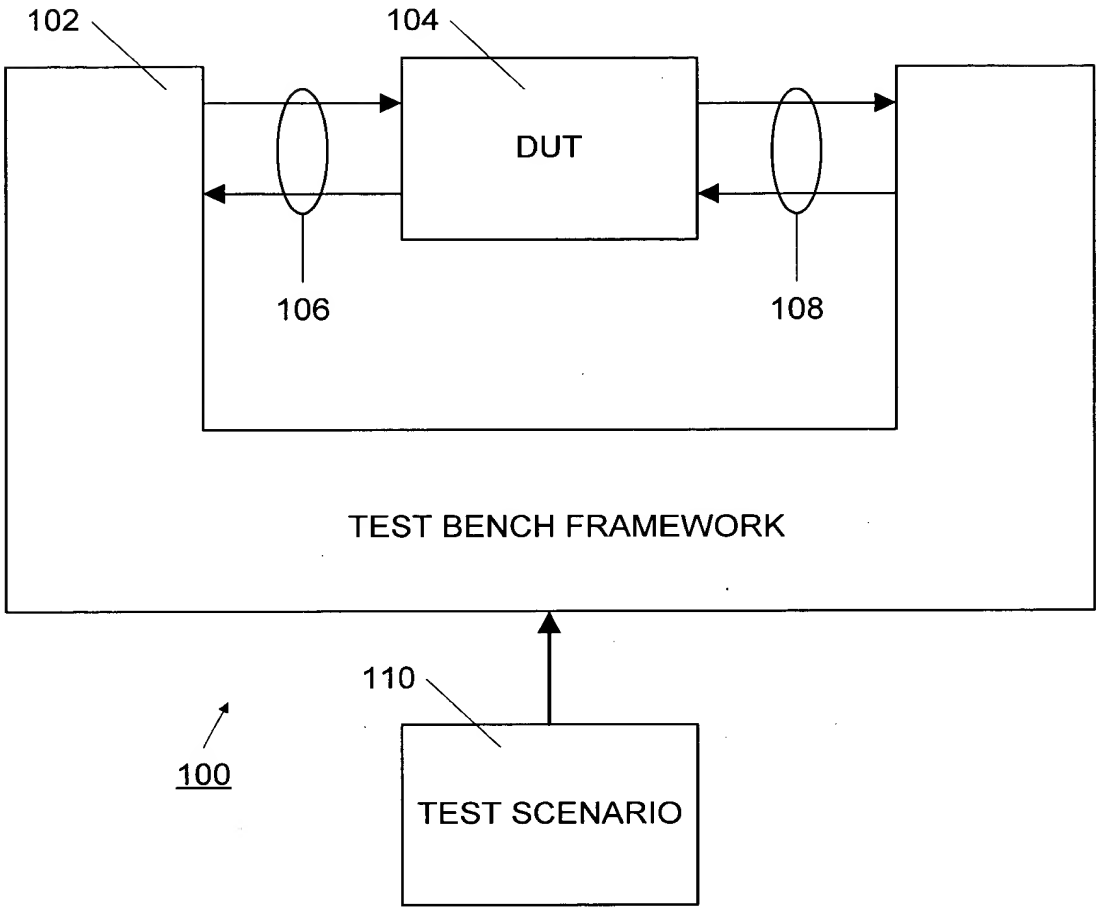

FIG. 1 (PRIOR ART)

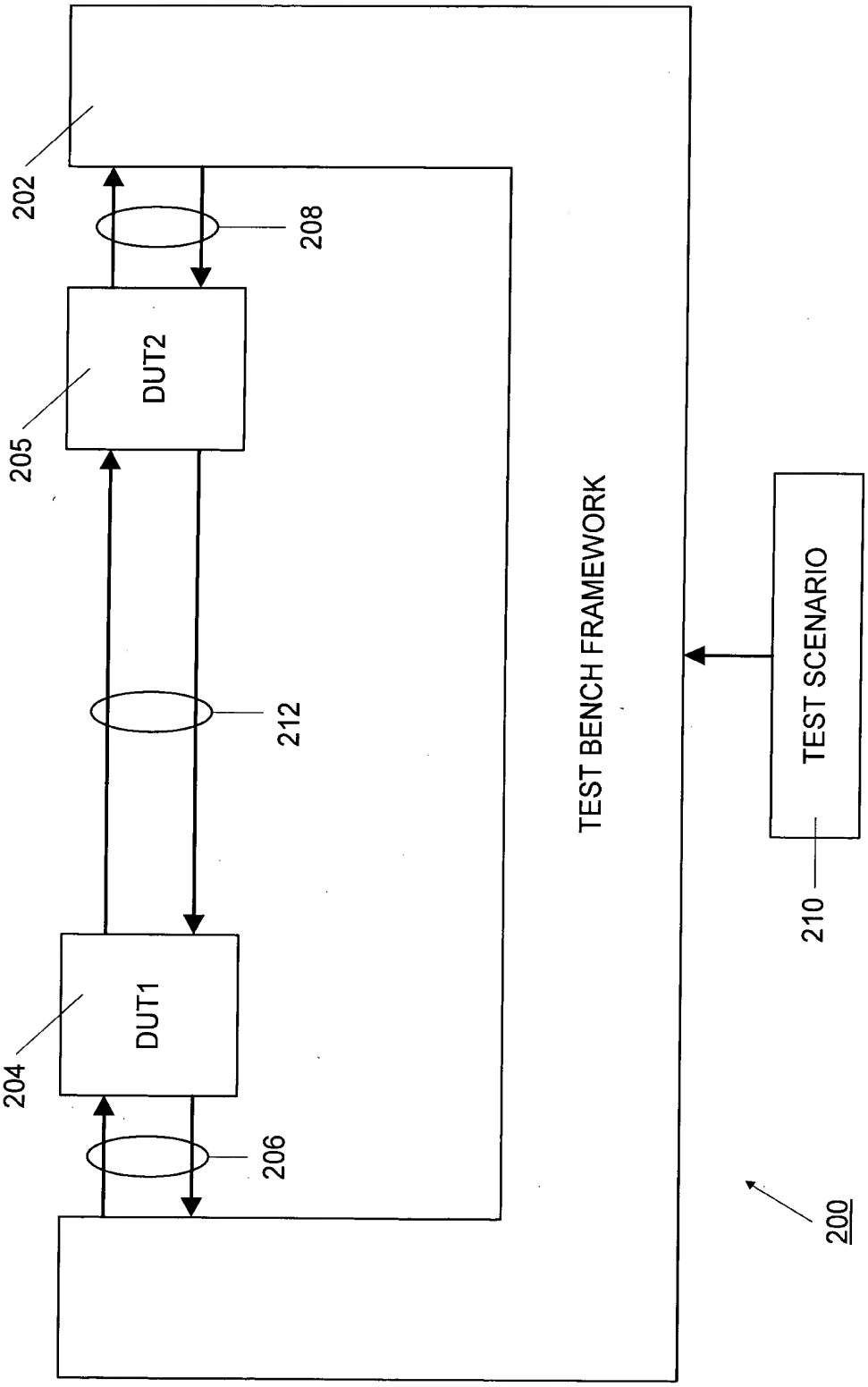

FIG. 2 (PRIOR ART)

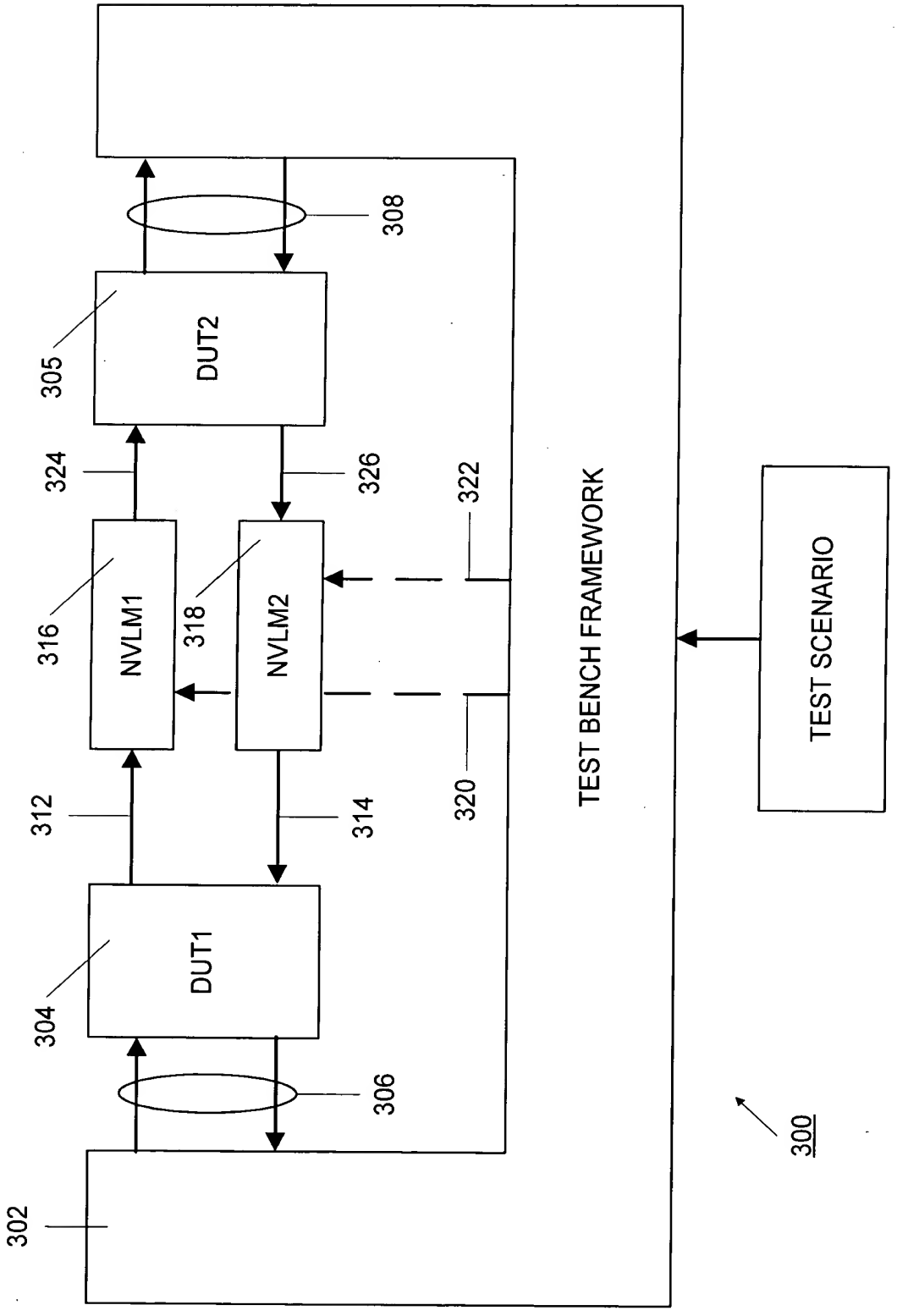
308
DUT2
305
324
326
322
NVLM1
316
318
NVLM2
TEST BENCH FRAMEWORK
TEST SCENARIO
312
314
320
DUT1
304
306
302
300

FIG. 3

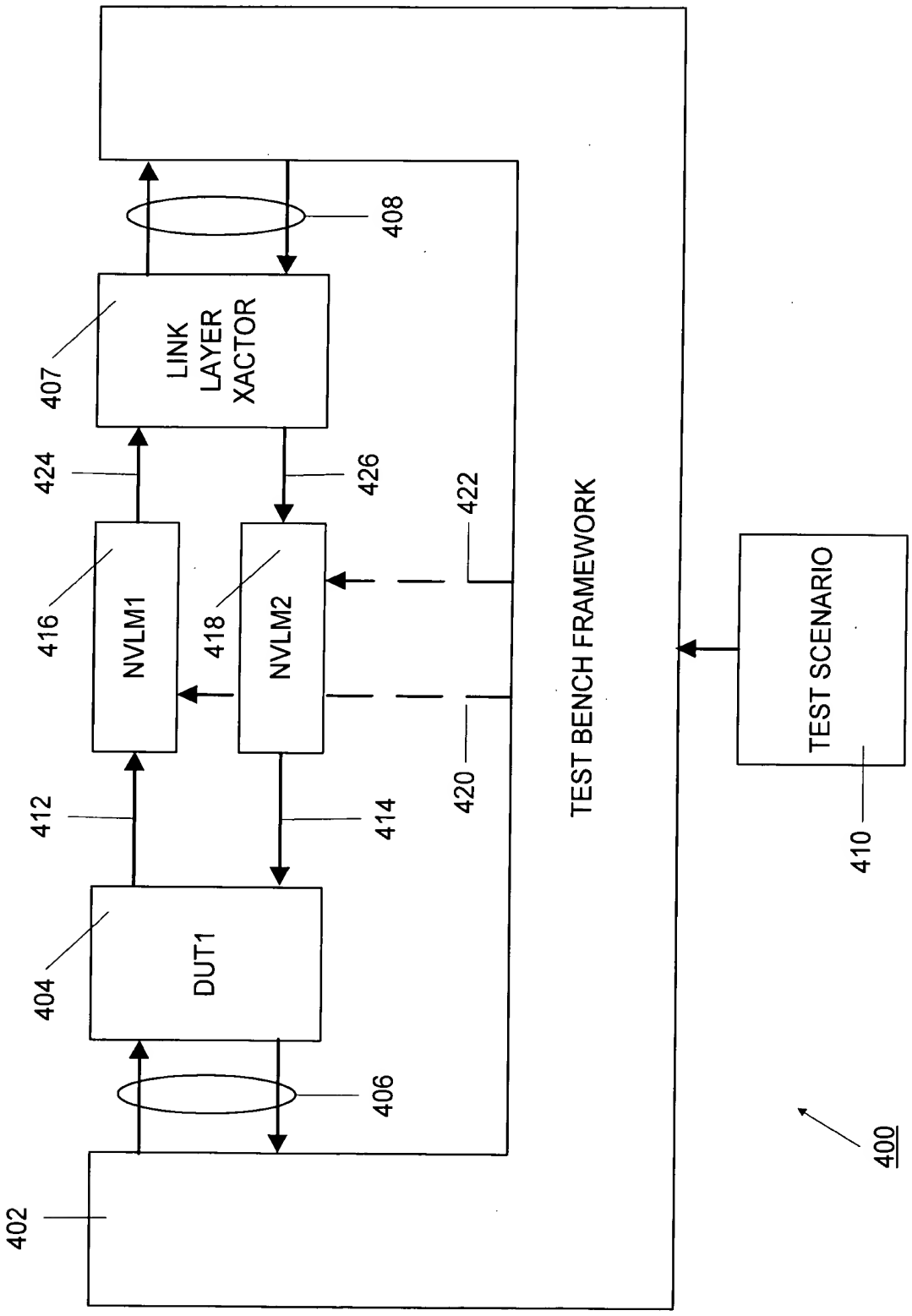
LINK LAYER XACTOR
NVLM1
NVLM2
DUT1
TEST BENCH FRAMEWORK
TEST SCENARIO
402
404
406
407
408
410
412
414
416
418
420
422
424
426
400

FIG. 4

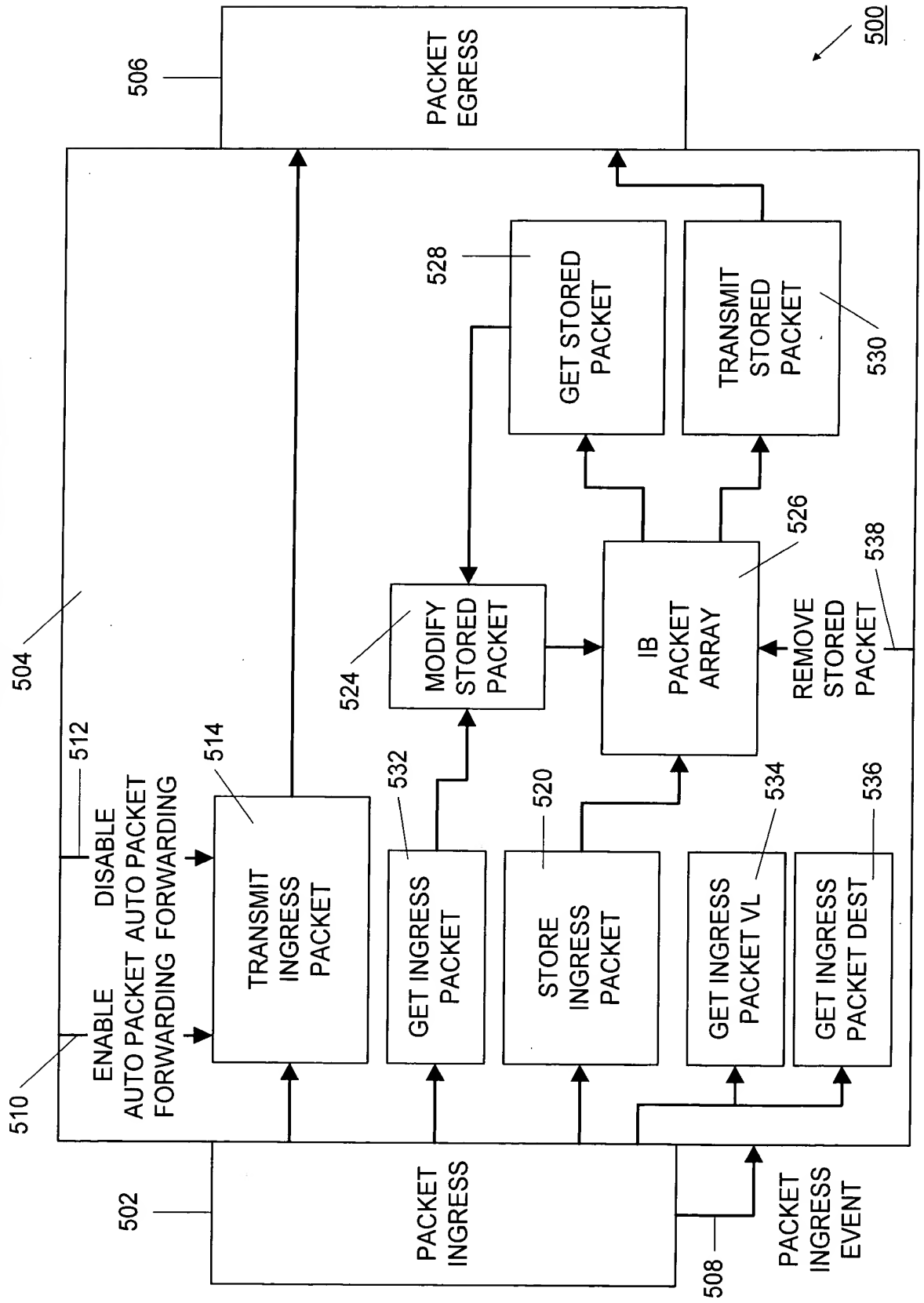
500
506
PACKET EGRESS
528
GET STORED PACKET
530
TRANSMIT STORED PACKET
526
538
REMOVE STORED PACKET
524
MODIFY STORED PACKET
IB PACKET ARRAY
504
512
514
532
520
534
536
510
ENABLE AUTO PACKET FORWARDING
DISABLE AUTO PACKET FORWARDING
TRANSMIT INGRESS PACKET
GET INGRESS PACKET
STORE INGRESS PACKET
GET INGRESS PACKET VL
GET INGRESS PACKET DEST
502
PACKET INGRESS
508
PACKET INGRESS EVENT

FIG. 5

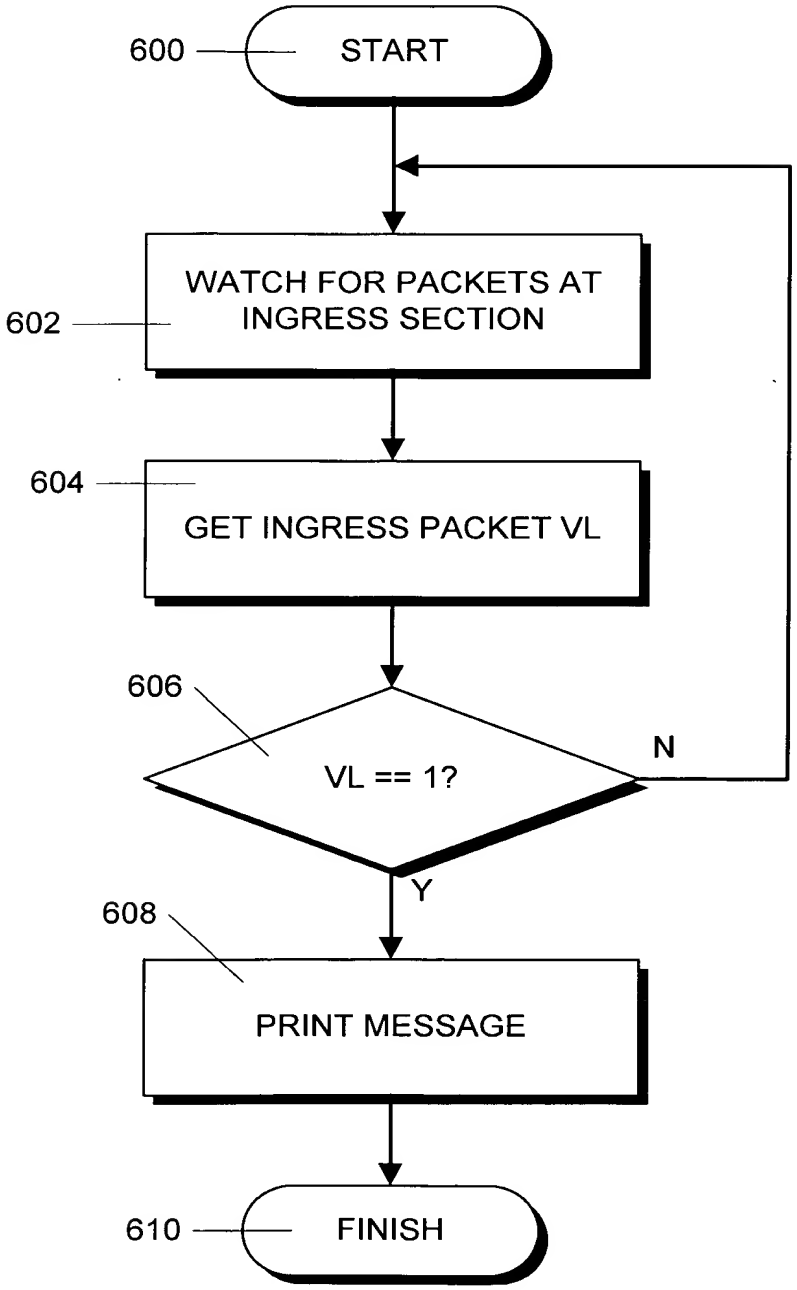
600
START
WATCH FOR PACKETS AT INGRESS SECTION
602
604
GET INGRESS PACKET VL
606
VL == 1?
N
Y
608
PRINT MESSAGE
610
FINISH

FIG. 6

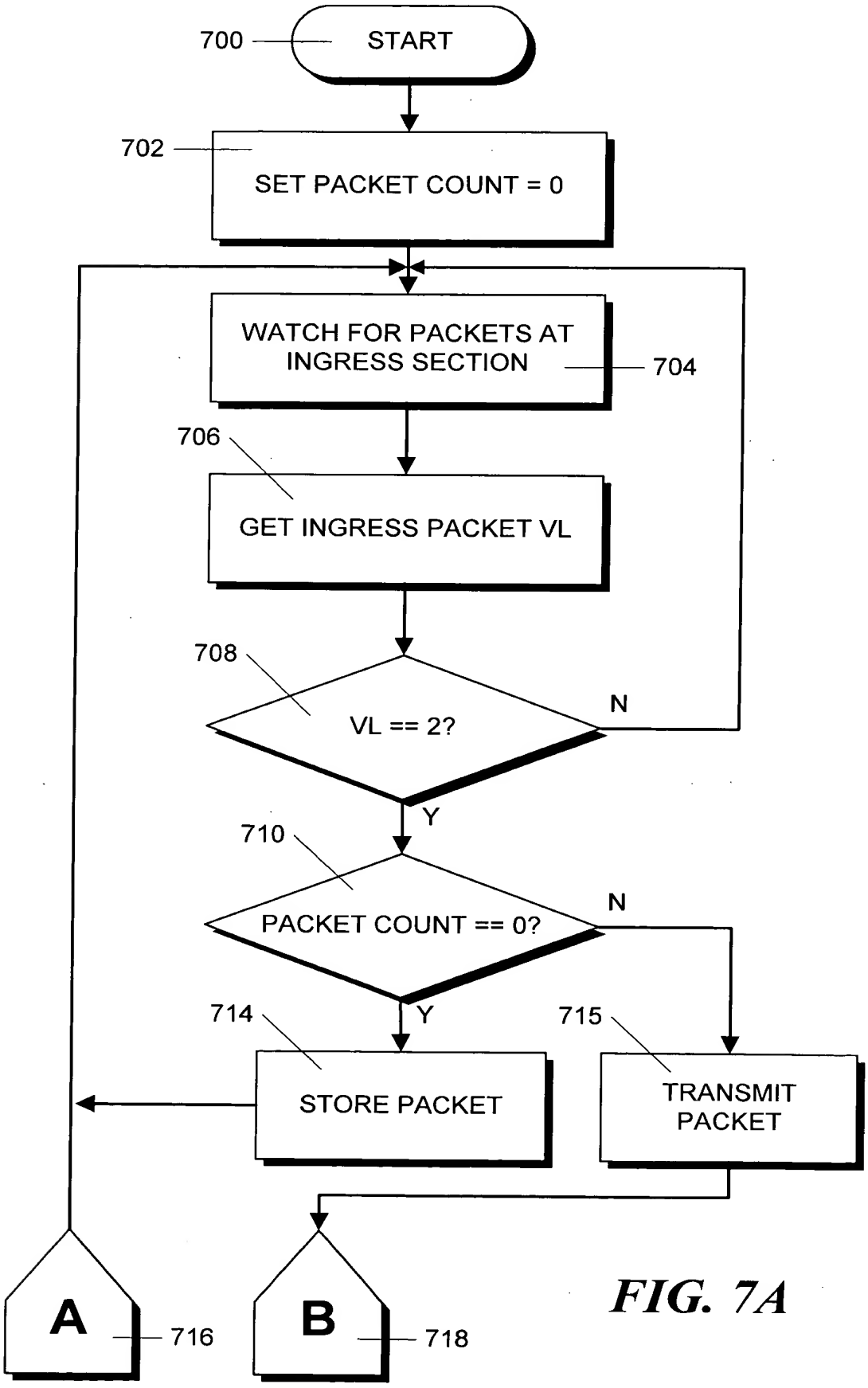
700
START
702
SET PACKET COUNT = 0
WATCH FOR PACKETS AT INGRESS SECTION
704
706
GET INGRESS PACKET VL
708
VL == 2?
N
Y
710
PACKET COUNT == 0?
N
Y
714
STORE PACKET
715
TRANSMIT PACKET
A
716
B
718

FIG. 7A

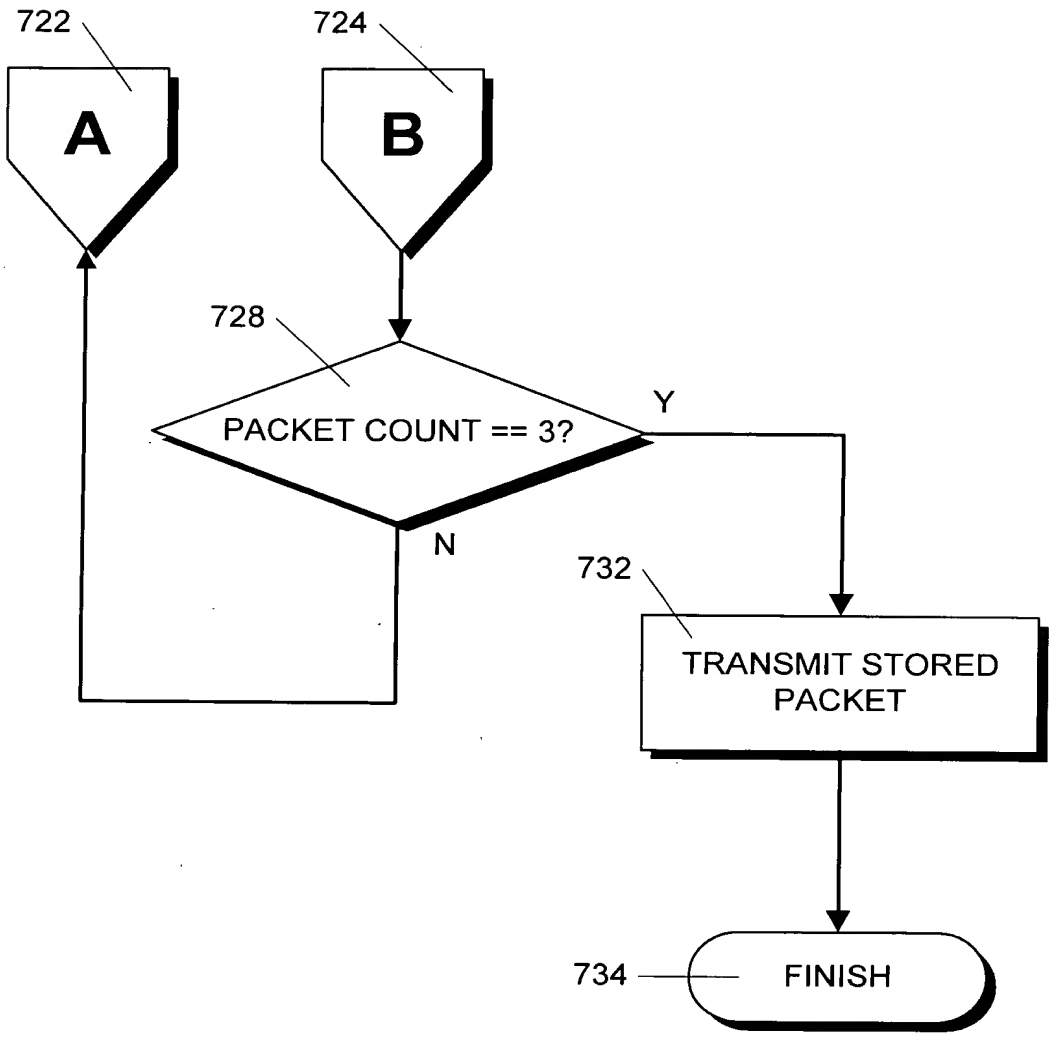
722
724
A
B
728
PACKET COUNT == 3?
Y
N
732
TRANSMIT STORED PACKET
734
FINISH

FIG. 7B

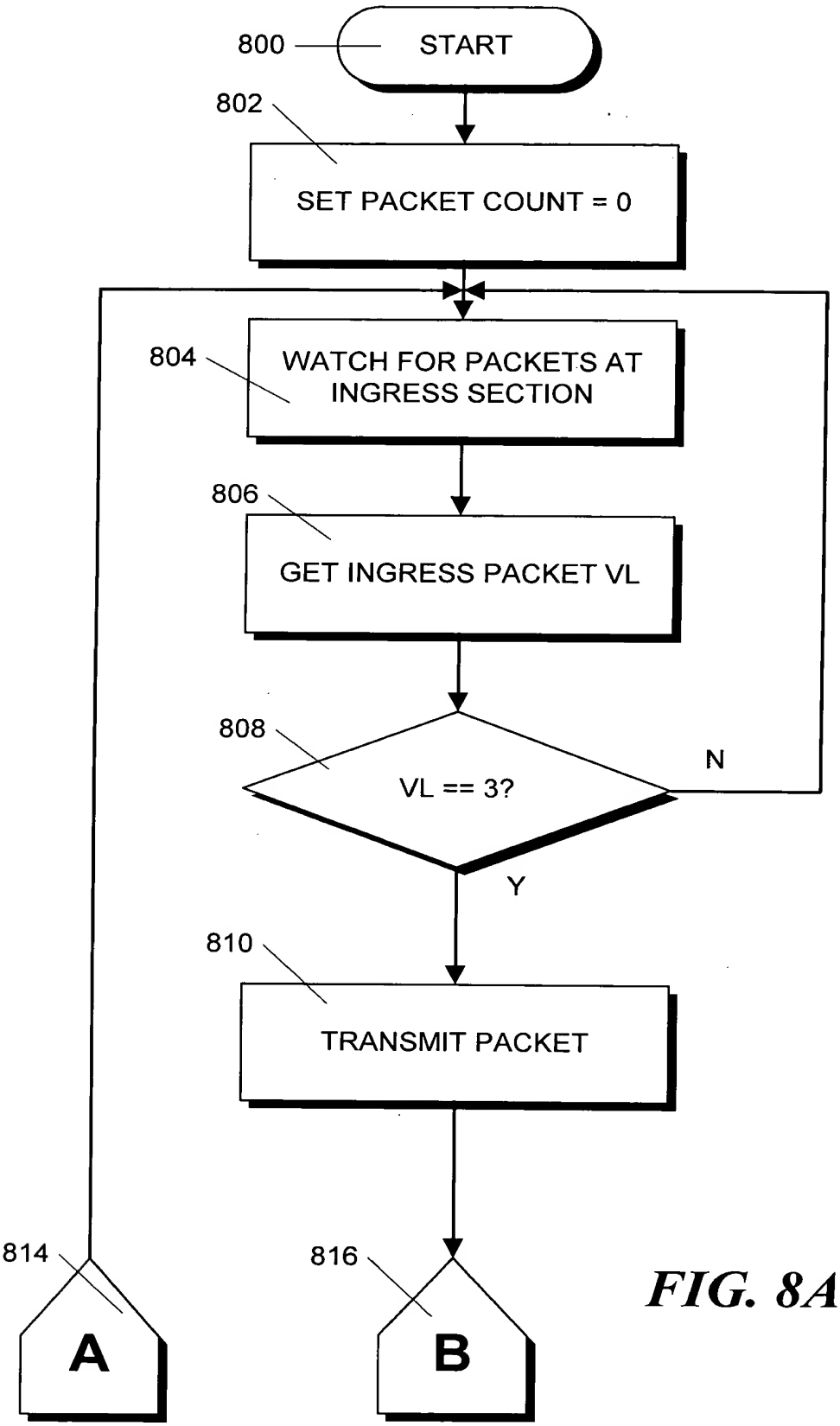
800
START
802
SET PACKET COUNT = 0
804
WATCH FOR PACKETS AT INGRESS SECTION
806
GET INGRESS PACKET VL
808
VL == 3?
N
Y
810
TRANSMIT PACKET
814
A
816
B

FIG. 8A

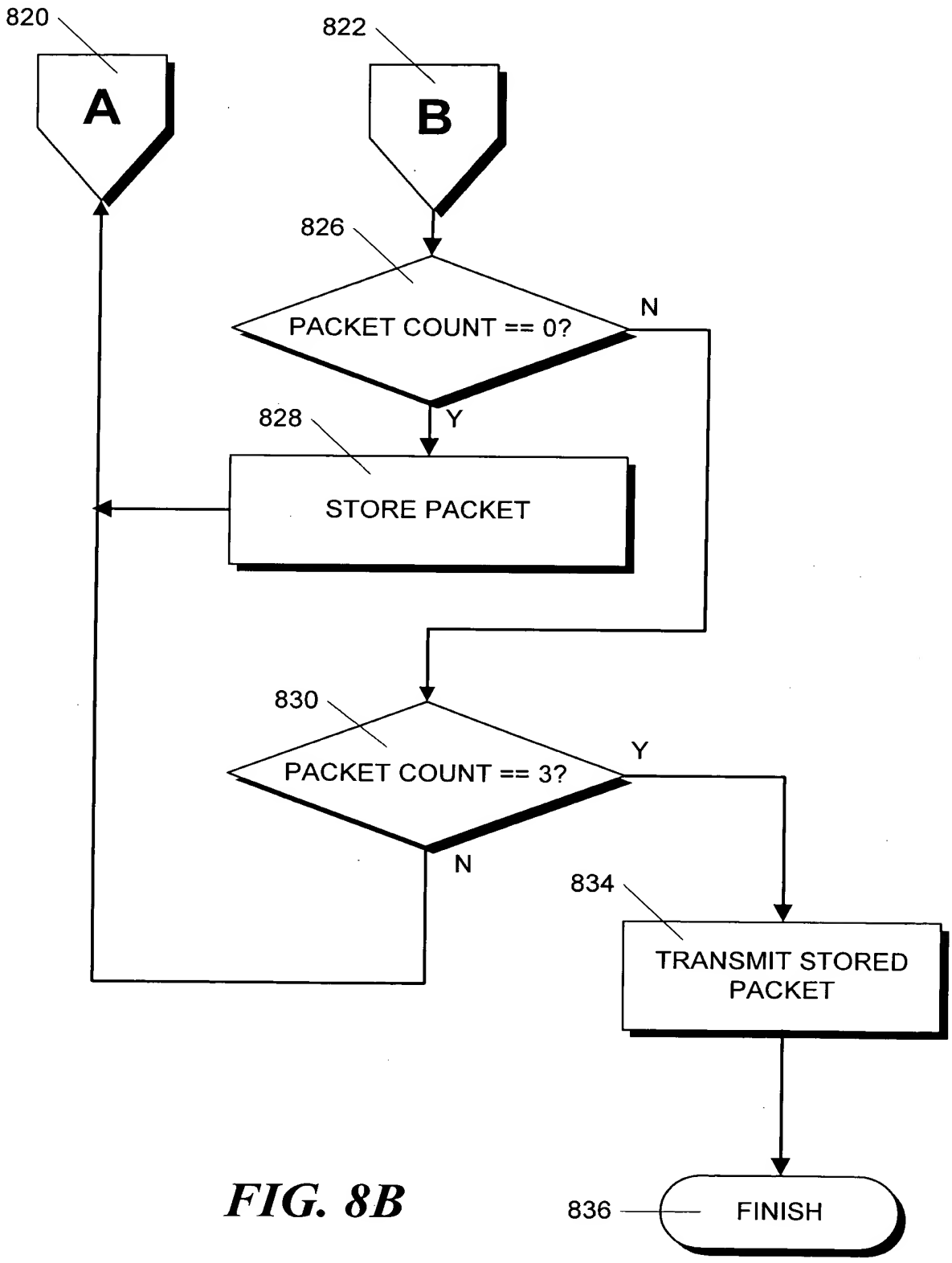
820
A
822
B
826
PACKET COUNT == 0?
N
Y
828
STORE PACKET
830
PACKET COUNT == 3?
Y
N
834
TRANSMIT STORED PACKET
836
FINISH

FIG. 8B

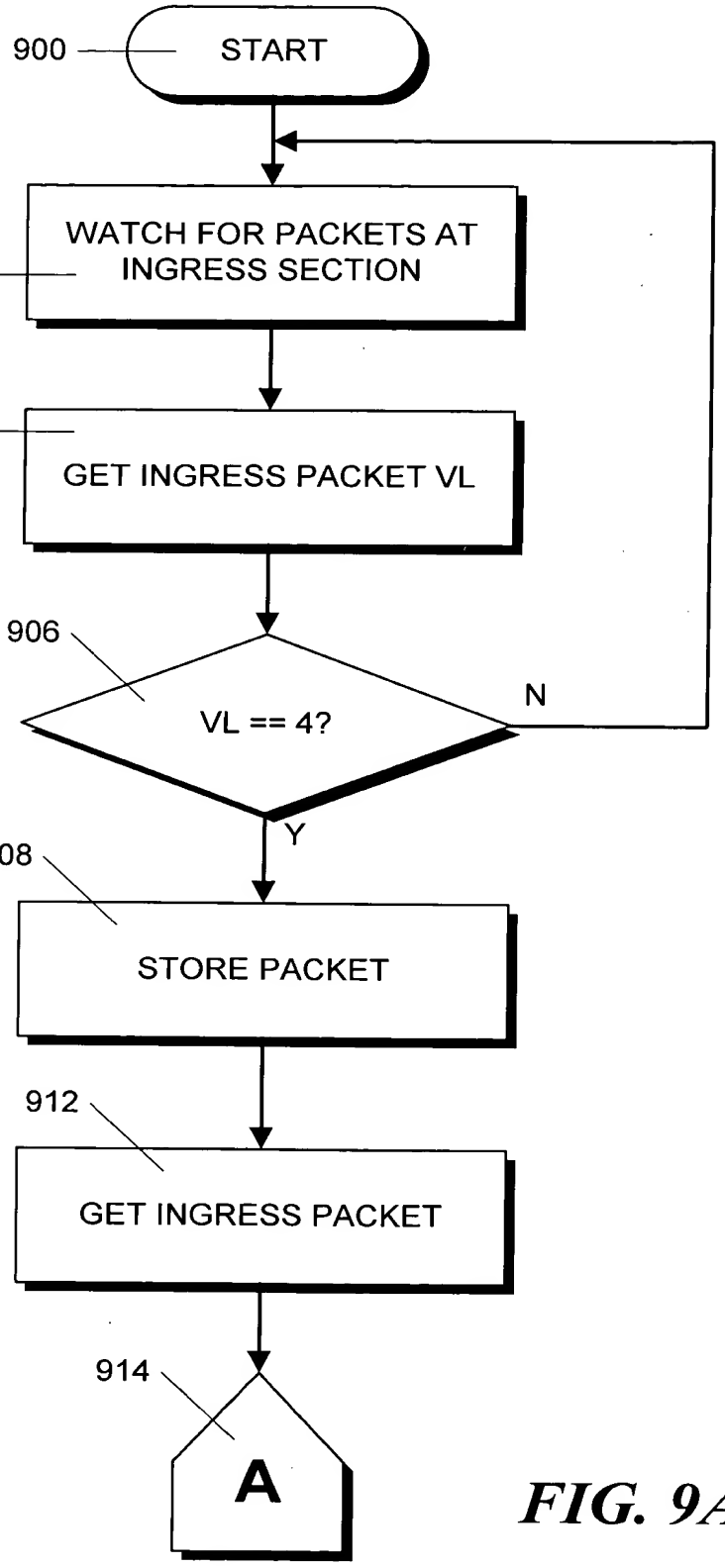
900
START
WATCH FOR PACKETS AT INGRESS SECTION
902
904
GET INGRESS PACKET VL
906
VL == 4?
N
Y
908
STORE PACKET
912
GET INGRESS PACKET
914
A

FIG. 9A

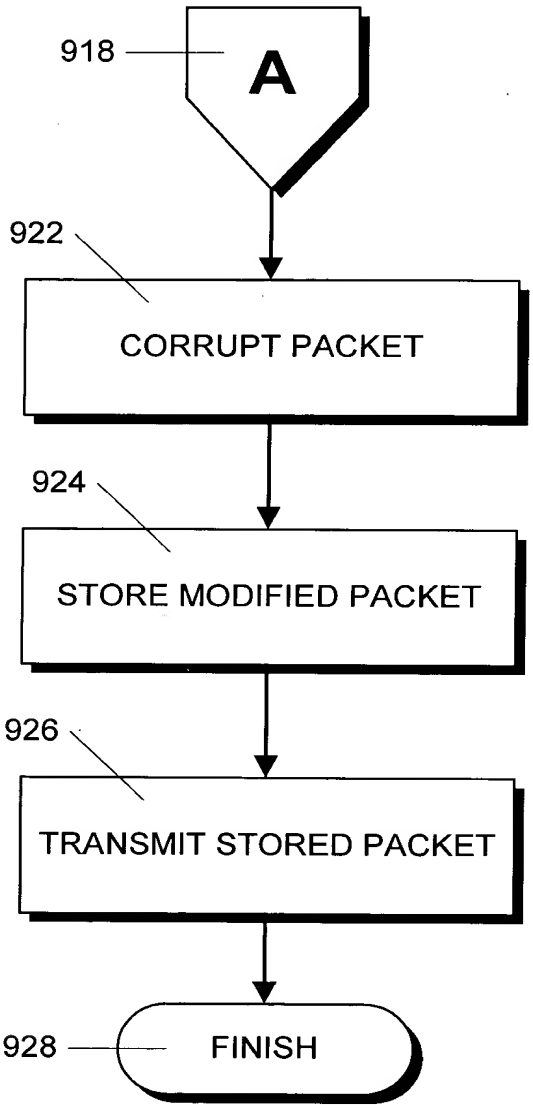
918
A
922
CORRUPT PACKET
924
STORE MODIFIED PACKET
926
TRANSMIT STORED PACKET
928
FINISH

FIG. 9B